

ABSTRACT OF THE DISCLOSURE

A reference voltage generation circuit includes
at least one reference cell having a source electrode and
5 a drain electrode; a plurality of first sense circuits
connected to the reference cell and including an N-channel
transistor, a P-channel transistor, a plurality of input
ends and a plurality of output ends; and a plurality of
second sense circuits each for receiving an output from
10 a corresponding one of the plurality of first sense circuits,
the plurality of second sense circuits each having a load
circuit, an N-channel transistor, a plurality of input
ends and a plurality of output ends.